

uniproc_fp_sched_no_termination.imi

Generated by IMITATOR 3.4-beta "Cheese Durian"
Build: develop/6908f5d
Generation time: Tue Jan 23, 2024 09:33:27

Initial		9 clocks		2 parameters		2 discrete	
$t1_c$	$t1_d$	$t2_c$	$t2_d$	$t1_arr_P \geq 0$	$t2_arr_P \geq 0$	$t1_arr_x = 0$	$t2_arr_x = 0$
$t1_urgent$	$t1_arr_x$	$t2_urgent$	$t2_arr_x$	$\& t1_arr_P \geq 0$	$\& t2_arr_P \geq 0$	$\& t1_d = 0$	$\& t2_d = 0$
$t1_arr_P = t1_arr_x$	$t1_urgent = 0$	$t2_arr_P = t2_arr_x$	$t2_urgent = 0$	$\& t1_c = 0$	$\& t2_c = 0$	$\& t1_arr_P = t1_arr_x$	$\& t2_arr_P = t2_arr_x$
$\& t1_arr_P = t1_arr_x$	$\& t1_urgent = 0$	$\& t2_arr_P = t2_arr_x$	$\& t2_urgent = 0$	$\& CPU1_urgent = 0$	$\& CPU1_urgent = 0$		
$t1_inst$	$t2_inst$						
$t1_arr_P$	$t2_arr_P$						
$CPU1_urgent$							

