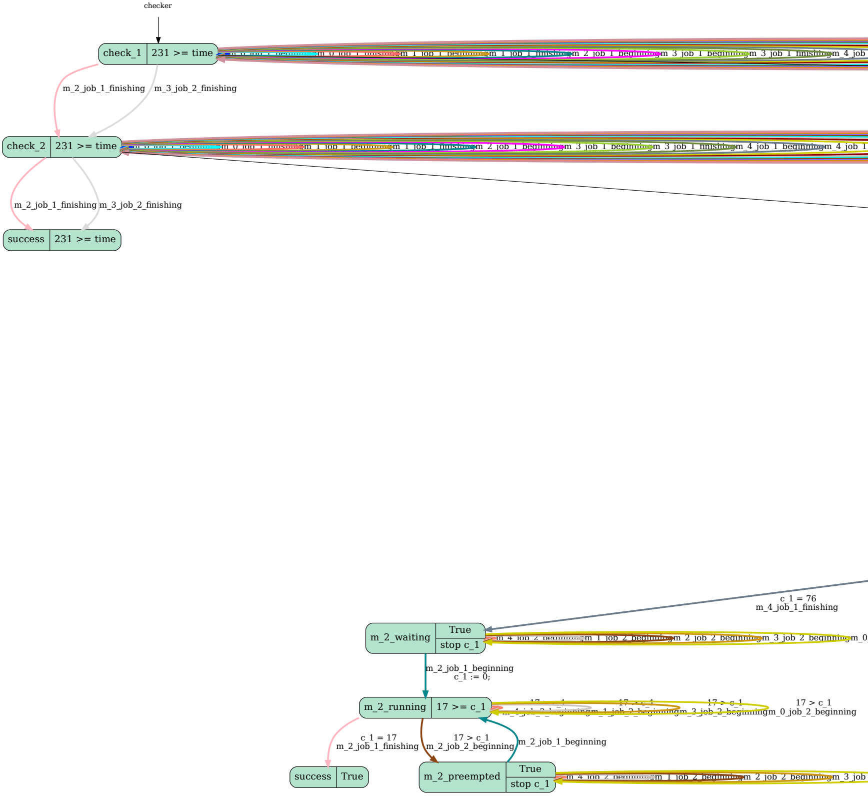
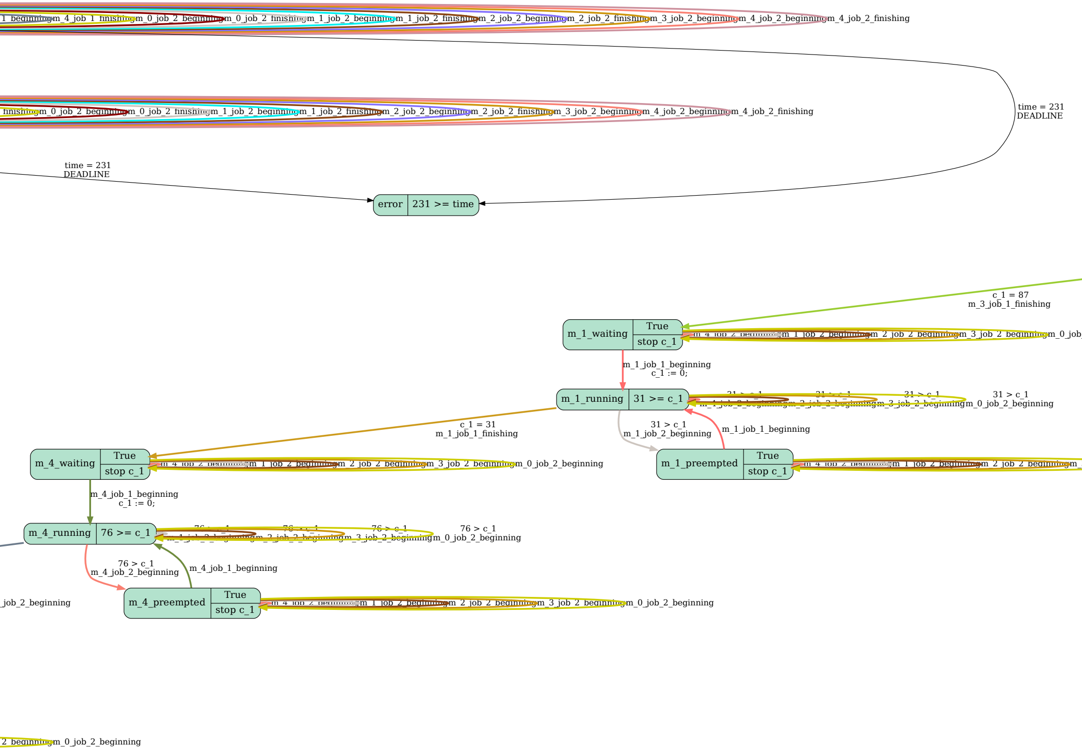
LA02-2_5param.imi

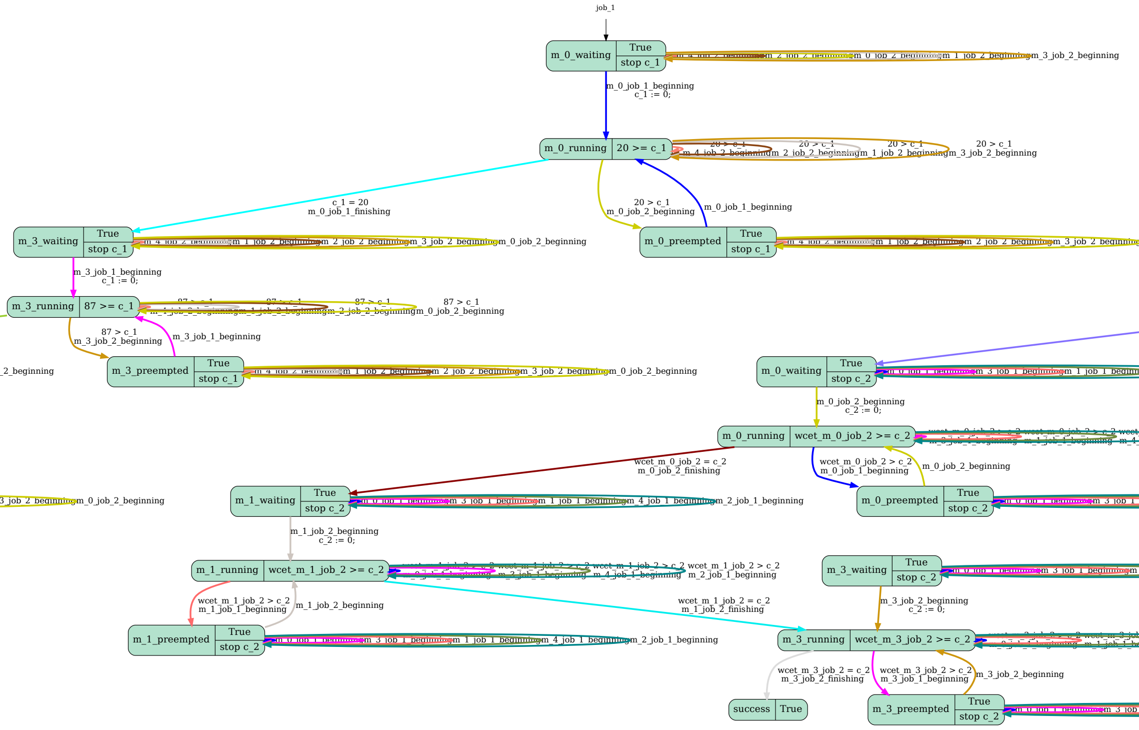
Generated by IMITATOR 3.4-beta "Cheese Durian"
Build: develop/6908f5d
Generation time: Tue Jan 23, 2024 09:33:35

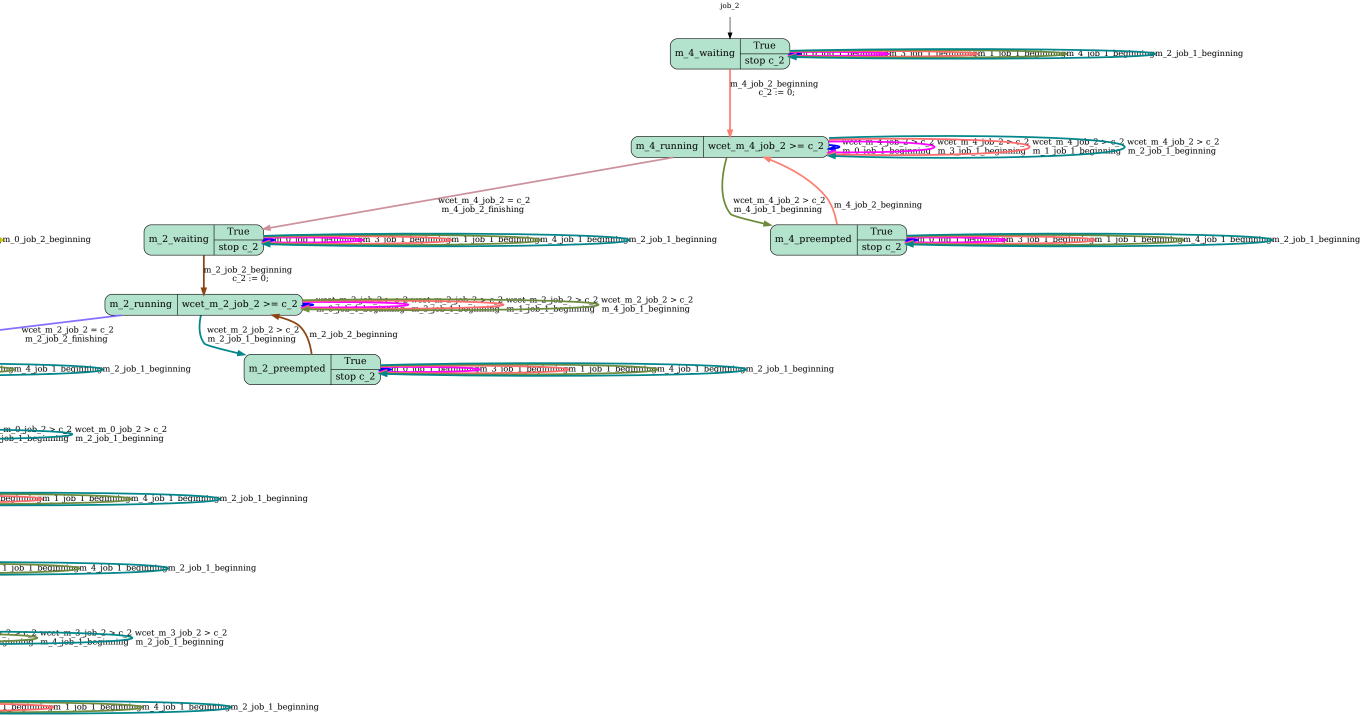
3 clocks	5 parameters	Initial
c_2 c_1 time	wcet_m_4_job_2 wcet_m_2_job_2 wcet_m_0_job_2 wcet_m_1_job_2 wcet_m_3_job_2	wcet_m_4_job_2 >= 0 & wcet_m_2_job_2 >= 0 & wcet_m_0_job_2 >= 0 & wcet_m_1_job_2 >= 0 & wcet_m_3_job_2 >= 0 & c_2 = 0 & c_1 = 0 & time = 0


checker
check_1
231 >= time
m_0_job_1_beginning
m_0_job_1_finishing
m_1_job_1_beginning
m_1_job_1_finishing
m_2_job_1_beginning
m_3_job_1_beginning
m_3_job_1_finishing
m_4_job
m_2_job_1_finishing
m_3_job_2_finishing
check_2
231 >= time
m_4_job_1_beginning
m_4_job_1
m_2_job_1_finishing
m_3_job_2_finishing
success
231 >= time
c_1 = 76
m_4_job_1_finishing
m_2_waiting
True
stop c_1
m_4_job_2_beginning
m_1_job_2_beginning
m_2_job_2_beginning
m_3_job_2_beginning
m_0
m_2_job_1_beginning
c_1 := 0;
m_2_running
17 >= c_1
17 > c_1
m_4_job_2_beginning
17 > c_1
m_1_job_2_beginning
17 > c_1
m_3_job_2_beginning
17 > c_1
m_0_job_2_beginning
c_1 = 17
m_2_job_1_finishing
17 > c_1
m_2_job_2_beginning
m_2_job_1_beginning
success
True
m_2_preempted
True
stop c_1
m_4_job_2_beginning
m_1_job_2_beginning
m_2_job_2_beginning
m_3_job



1_beginning m_4_job_1_finishing m_0_job_2_beginning m_0_job_2_finishing m_1_job_2_beginning m_1_job_2_finishing m_2_job_2_beginning m_2_job_2_finishing m_3_job_2_beginning m_4_job_2_beginning m_4_job_2_finishing
time = 231
DEADLINE
finishing m_0_job_2_beginning m_0_job_2_finishing m_1_job_2_beginning m_1_job_2_finishing m_2_job_2_beginning m_2_job_2_finishing m_3_job_2_beginning m_4_job_2_beginning m_4_job_2_finishing
time = 231
DEADLINE
error
231 >= time
c_1 = 87
m_3_job_1_finishing
m_1_waiting
True
stop c_1
m_4_job_2_beginning m_1_job_2_beginning m_2_job_2_beginning m_3_job_2_beginning m_0_job
m_1_job_1_beginning
c_1 := 0;
m_1_running
31 >= c_1
31 > c_1
m_1_job_2_beginning m_2_job_2_beginning m_3_job_2_beginning m_0_job_2_beginning
c_1 = 31
m_1_job_1_finishing
31 > c_1
m_1_job_2_beginning
m_1_job_1_beginning
m_4_waiting
True
stop c_1
m_4_job_2_beginning m_1_job_2_beginning m_2_job_2_beginning m_3_job_2_beginning m_0_job_2_beginning
m_1_preempted
True
stop c_1
m_4_job_2_beginning m_1_job_2_beginning m_2_job_2_beginning m_
m_4_job_1_beginning
c_1 := 0;
m_4_running
76 >= c_1
76 > c_1
m_1_job_2_beginning m_2_job_2_beginning m_3_job_2_beginning m_0_job_2_beginning
76 > c_1
m_4_job_2_beginning
m_4_job_1_beginning
_job_2_beginning
m_4_preempted
True
stop c_1
m_4_job_2_beginning m_1_job_2_beginning m_2_job_2_beginning m_3_job_2_beginning m_0_job_2_beginning
2_beginning m_0_job_2_beginning


job_1
m_0_waiting
True
stop c_1
m_4_job_2_beginning
m_2_job_2_beginning
m_0_job_2_beginning
m_1_job_2_beginning
m_3_job_2_beginning
m_0_job_1_beginning
c_1 := 0;
m_0_running
20 >= c_1
20 > c_1
m_4_job_2_beginning
20 > c_1
m_2_job_2_beginning
20 > c_1
m_1_job_2_beginning
20 > c_1
m_3_job_2_beginning
c_1 = 20
m_0_job_1_finishing
20 > c_1
m_0_job_2_beginning
m_0_job_1_beginning
m_3_waiting
True
stop c_1
m_4_job_2_beginning
m_1_job_2_beginning
m_2_job_2_beginning
m_3_job_2_beginning
m_0_job_2_beginning
m_0_preempted
True
stop c_1
m_4_job_2_beginning
m_1_job_2_beginning
m_2_job_2_beginning
m_3_job_2_beginning
m_3_job_1_beginning
c_1 := 0;
m_3_running
87 >= c_1
87 > c_1
m_4_job_2_beginning
87 > c_1
m_1_job_2_beginning
87 > c_1
m_2_job_2_beginning
87 > c_1
m_0_job_2_beginning
87 > c_1
m_3_job_2_beginning
m_3_job_1_beginning
_2_beginning
m_3_preempted
True
stop c_1
m_4_job_2_beginning
m_1_job_2_beginning
m_2_job_2_beginning
m_3_job_2_beginning
m_0_job_2_beginning
m_0_waiting
True
stop c_2
m_0_job_1_beginning
m_3_job_1_beginning
m_1_job_1_beginning
m_0_job_2_beginning
c_2 := 0;
m_0_running
wcet_m_0_job_2 >= c_2
wcet_m_0_job_2 > c_2
wcet_m_0_job_2 > c_2
m_3_job_1_beginning
m_1_job_1_beginning
wcet_m_0_job_2 = c_2
m_0_job_2_finishing
wcet_m_0_job_2 > c_2
m_0_job_1_beginning
m_0_job_2_beginning
3_job_2_beginning
m_0_job_2_beginning
m_1_waiting
True
stop c_2
m_0_job_1_beginning
m_3_job_1_beginning
m_1_job_1_beginning
m_4_job_1_beginning
m_2_job_1_beginning
m_0_preempted
True
stop c_2
m_0_job_1_beginning
m_3_job_1
m_1_job_2_beginning
c_2 := 0;
m_1_running
wcet_m_1_job_2 >= c_2
wcet_m_1_job_2 > c_2
m_0_job_1_beginning
wcet_m_1_job_2 > c_2
m_3_job_1_beginning
wcet_m_1_job_2 > c_2
m_4_job_1_beginning
wcet_m_1_job_2 > c_2
m_2_job_1_beginning
m_3_waiting
True
stop c_2
m_0_job_1_beginning
m_3_job_1_beginning
wcet_m_1_job_2 > c_2
m_1_job_1_beginning
m_1_job_2_beginning
wcet_m_1_job_2 = c_2
m_1_job_2_finishing
m_3_job_2_beginning
c_2 := 0;
m_1_preempted
True
stop c_2
m_0_job_1_beginning
m_3_job_1_beginning
m_1_job_1_beginning
m_4_job_1_beginning
m_2_job_1_beginning
m_3_running
wcet_m_3_job_2 >= c_2
wcet_m_3_job_2 = c_2
m_3_job_2_finishing
wcet_m_3_job_2 > c_2
m_3_job_1_beginning
m_3_job_2_beginning
success
True
m_3_preempted
True
stop c_2
m_0_job_1_beginning
m_3_job


job_2
m_4_waiting
True
stop c_2
m_4_job_2_beginning
c_2 := 0;
m_4_running
wcet_m_4_job_2 >= c_2
wcet_m_4_job_2 > c_2
m_0_job_1_beginning
m_3_job_1_beginning
m_1_job_1_beginning
m_2_job_1_beginning
wcet_m_4_job_2 = c_2
m_4_job_2_finishing
m_4_job_1_beginning
m_4_preempted
m_2_waiting
m_0_job_2_beginning
m_2_job_2_beginning
m_2_running
wcet_m_2_job_2 >= c_2
wcet_m_2_job_2 > c_2
wcet_m_2_job_2 = c_2
m_2_job_2_finishing
m_2_preempted
wcet_m_0_job_2 > c_2
wcet_m_3_job_2 > c_2