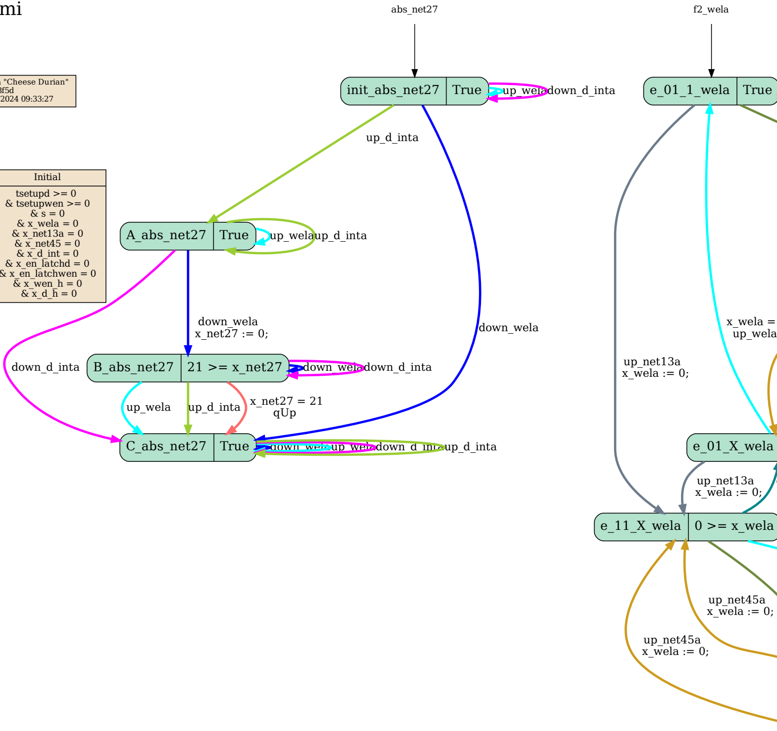
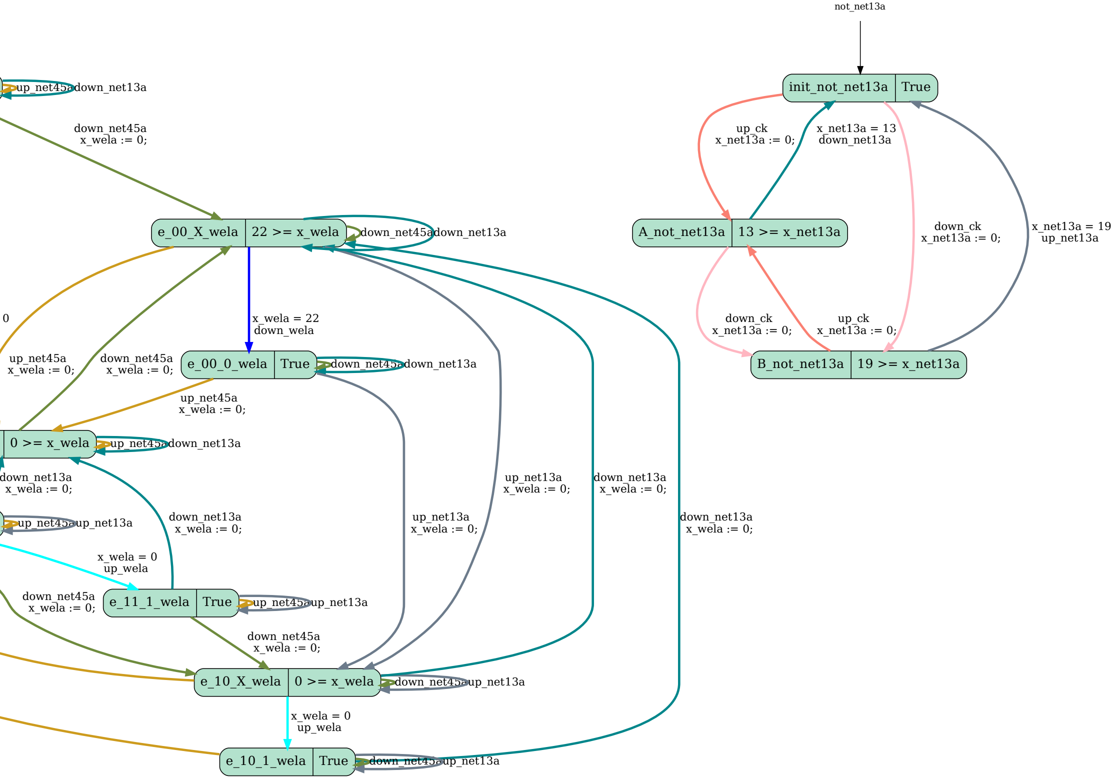
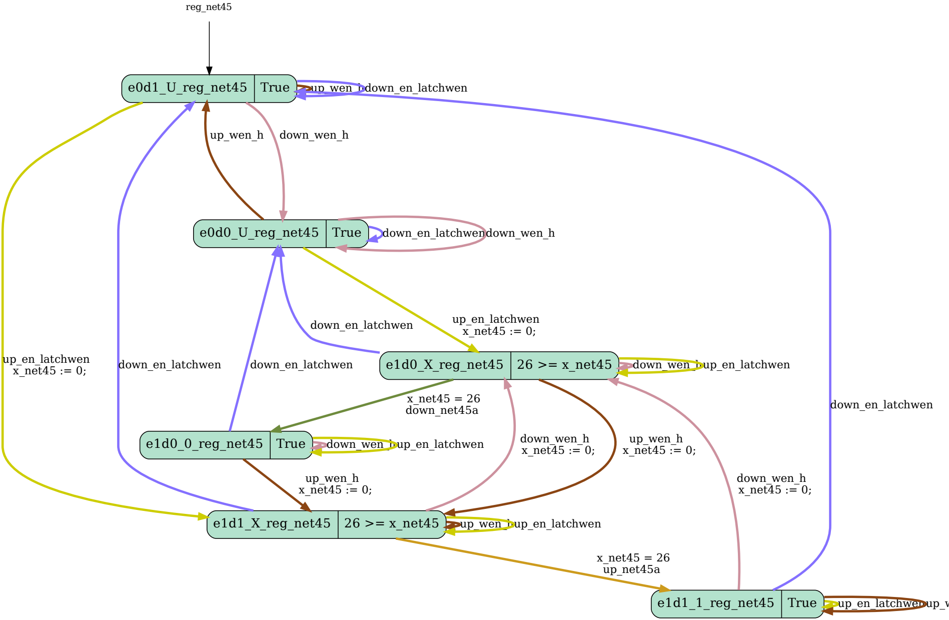
spsmall.imi

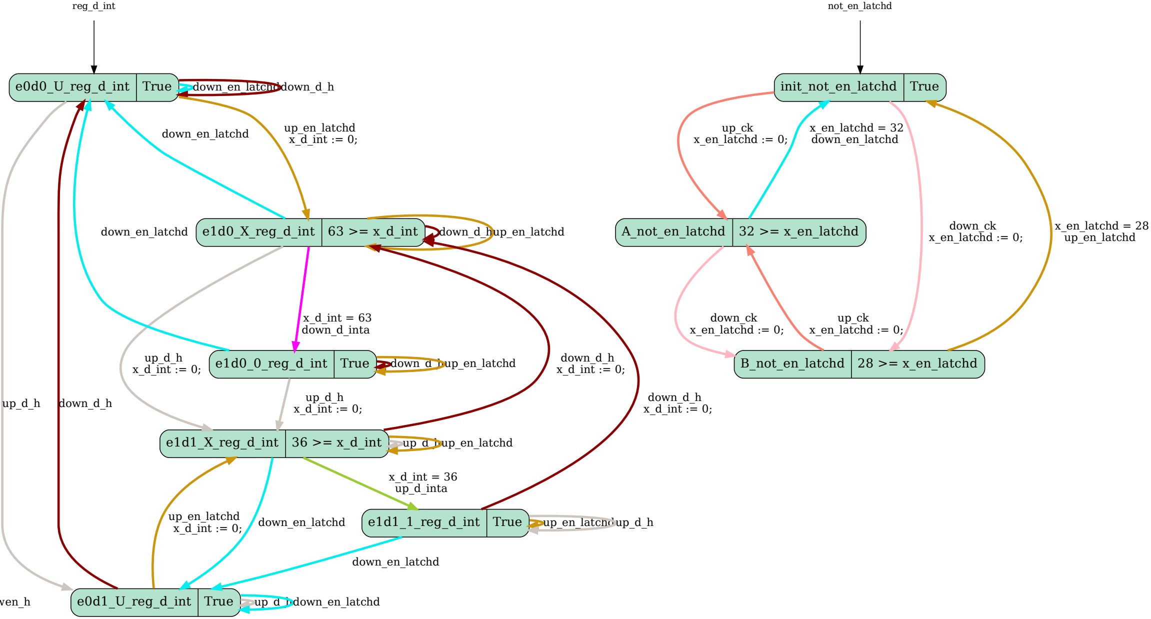
Generated by IMITATOR 3.4-beta "Cheese Durian"
Build: develop/6908f5d
Generation time: Tue Jan 23, 2024 09:33:27

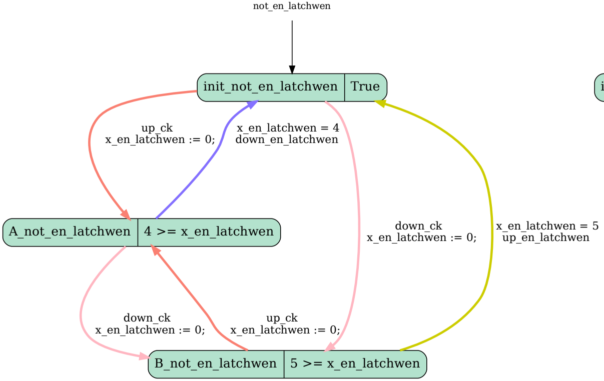
11 clocks	2 parameters	Initial
s x_net27 x_wela x_net13a x_net45 x_d_int x_en_latchd x_en_latchwen x_wen_h x_d_h x_observer	tsetupd tsetupwen	tsetupd >= 0 & tsetupwen >= 0 & s = 0 & x_wela = 0 & x_net13a = 0 & x_net45 = 0 & x_d_int = 0 & x_en_latchd = 0 & x_en_latchwen = 0 & x_wen_h = 0 & x_d_h = 0

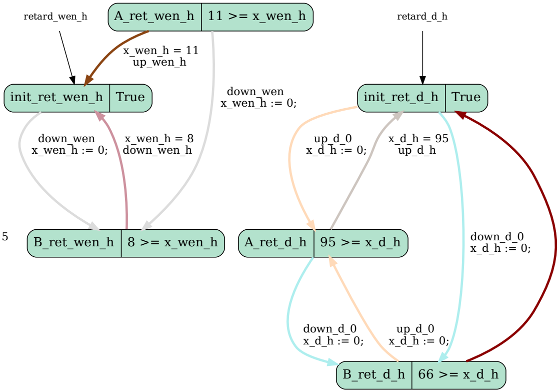

abs_net27
f2_wela
init_abs_net27 | True
up_wela
down_d_inta
up_d_inta
down_wela
e_01_1_wela | True
A_abs_net27 | True
up_wela
up_d_inta
down_wela
x_net27 := 0;
B_abs_net27 | 21 >= x_net27
down_wela
down_d_inta
down_d_inta
up_wela
up_d_inta
x_net27 = 21
qUp
C_abs_net27 | True
down_wela
up_wela
down_d_inta
up_d_inta
x_wela =
up_wela
up_net13a
x_wela := 0;
e_01_X_wela
up_net13a
x_wela := 0;
e_11_X_wela | 0 >= x_wela
up_net45a
x_wela := 0;
up_net45a
x_wela := 0;

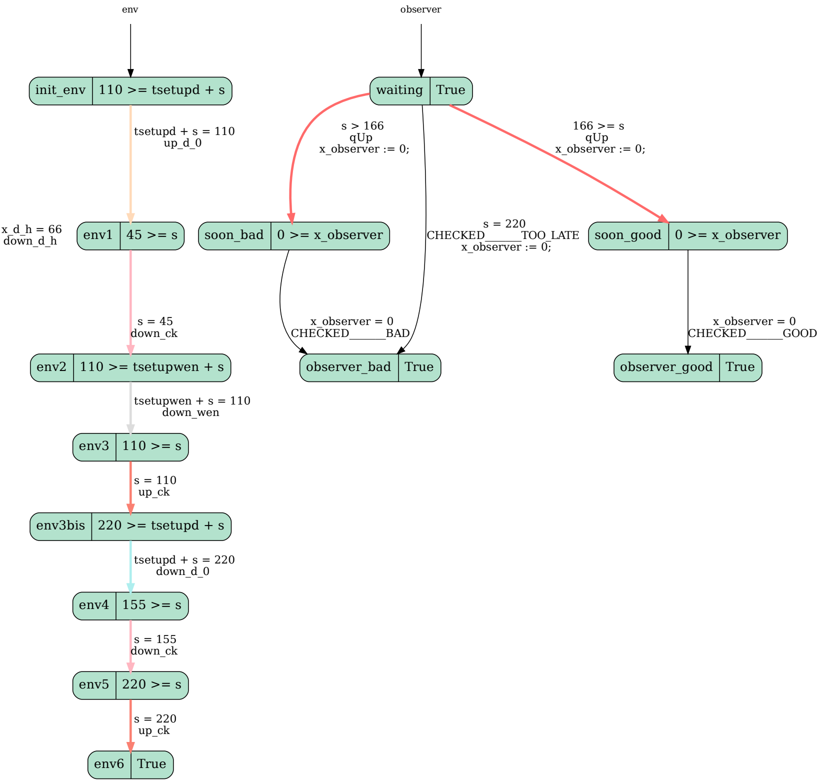

not_net13a
init_not_net13a
True
up_ck
x_net13a := 0;
x_net13a = 13
down_net13a
A_not_net13a
13 >= x_net13a
down_ck
x_net13a := 0;
x_net13a = 19
up_net13a
down_ck
x_net13a := 0;
up_ck
x_net13a := 0;
B_not_net13a
19 >= x_net13a
up_net45a
down_net13a
down_net45a
x_wela := 0;
e_00_X_wela
22 >= x_wela
down_net45a
down_net13a
x_wela = 22
down_wela
0
up_net45a
x_wela := 0;
down_net45a
x_wela := 0;
e_00_0_wela
True
down_net45a
down_net13a
up_net45a
x_wela := 0;
0 >= x_wela
up_net45a
down_net13a
down_net13a
x_wela := 0;
up_net13a
x_wela := 0;
down_net13a
x_wela := 0;
down_net13a
x_wela := 0;
up_net13a
x_wela := 0;
down_net13a
x_wela := 0;
up_net45a
up_net13a
x_wela = 0
up_wela
down_net45a
x_wela := 0;
e_11_1_wela
True
up_net45a
up_net13a
down_net45a
x_wela := 0;
e_10_X_wela
0 >= x_wela
down_net45a
up_net13a
x_wela = 0
up_wela
e_10_1_wela
True
down_net45a
up_net13a


reg_net45
e0d1_U_reg_net45 | True
up_wen_h
down_en_latchwen
up_wen_h
down_wen_h
e0d0_U_reg_net45 | True
down_en_latchwen
down_wen_h
up_en_latchwen
x_net45 := 0;
down_en_latchwen
e1d0_X_reg_net45 | 26 >= x_net45
down_wen_h
up_en_latchwen
up_en_latchwen
x_net45 := 0;
down_en_latchwen
down_en_latchwen
down_en_latchwen
x_net45 = 26
down_net45a
e1d0_0_reg_net45 | True
down_wen_h
up_en_latchwen
down_wen_h
x_net45 := 0;
up_wen_h
x_net45 := 0;
down_wen_h
x_net45 := 0;
up_wen_h
x_net45 := 0;
e1d1_X_reg_net45 | 26 >= x_net45
up_wen_h
up_en_latchwen
x_net45 = 26
up_net45a
e1d1_1_reg_net45 | True
up_en_latchwen
up_w


reg_d_int
not_en_latchd
e0d0_U_reg_d_int | True
down_en_latchd
down_d_h
up_en_latchd
x_d_int := 0;
down_en_latchd
e1d0_X_reg_d_int | 63 >= x_d_int
down_d_h
up_en_latchd
down_en_latchd
x_d_int = 63
down_d_inta
e1d0_0_reg_d_int | True
down_d_h
up_en_latchd
up_d_h
x_d_int := 0;
down_d_h
x_d_int := 0;
up_d_h
x_d_int := 0;
down_d_h
x_d_int := 0;
up_d_h
down_d_h
e1d1_X_reg_d_int | 36 >= x_d_int
up_d_h
up_en_latchd
x_d_int = 36
up_d_inta
up_en_latchd
x_d_int := 0;
down_en_latchd
e1d1_1_reg_d_int | True
up_en_latchd
up_d_h
down_en_latchd
e0d1_U_reg_d_int | True
up_d_h
down_en_latchd
wen_h
init_not_en_latchd | True
up_ck
x_en_latchd := 0;
x_en_latchd = 32
down_en_latchd
A_not_en_latchd | 32 >= x_en_latchd
down_ck
x_en_latchd := 0;
x_en_latchd = 28
up_en_latchd
down_ck
x_en_latchd := 0;
up_ck
x_en_latchd := 0;
B_not_en_latchd | 28 >= x_en_latchd


not_en_latchwen
init_not_en_latchwen | True
up_ck
x_en_latchwen := 0;
x_en_latchwen = 4
down_en_latchwen
A_not_en_latchwen | 4 >= x_en_latchwen
down_ck
x_en_latchwen := 0;
x_en_latchwen = 5
up_en_latchwen
down_ck
x_en_latchwen := 0;
up_ck
x_en_latchwen := 0;
B_not_en_latchwen | 5 >= x_en_latchwen


retard_wen_h
A_ret_wen_h | 11 >= x_wen_h
x_wen_h = 11
up_wen_h
down_wen
x_wen_h := 0;
init_ret_wen_h | True
down_wen
x_wen_h := 0;
x_wen_h = 8
down_wen_h
B_ret_wen_h | 8 >= x_wen_h
retard_d_h
init_ret_d_h | True
up_d_0
x_d_h := 0;
x_d_h = 95
up_d_h
A_ret_d_h | 95 >= x_d_h
down_d_0
x_d_h := 0;
down_d_0
x_d_h := 0;
up_d_0
x_d_h := 0;
B_ret_d_h | 66 >= x_d_h


env
observer
init_env
110 >= tsetupd + s
waiting
True
tsetupd + s = 110
up_d_0
s > 166
qUp
x_observer := 0;
166 >= s
qUp
x_observer := 0;
x_d_h = 66
down_d_h
env1
45 >= s
soon_bad
0 >= x_observer
s = 220
CHECKED______TOO_LATE
x_observer := 0;
soon_good
0 >= x_observer
s = 45
down_ck
x_observer = 0
CHECKED______BAD
x_observer = 0
CHECKED______GOOD
env2
110 >= tsetupwen + s
observer_bad
True
observer_good
True
tsetupwen + s = 110
down_wen
env3
110 >= s
s = 110
up_ck
env3bis
220 >= tsetupd + s
tsetupd + s = 220
down_d_0
env4
155 >= s
s = 155
down_ck
env5
220 >= s
s = 220
up_ck
env6
True