fischerHRSV02_3.imi

3 clocks	4 parameters	1 discrete	Initial
x_1 x_2 x_3	min_rw max_rw min_delay max_delay	lock	min_rw >= 0 & min_delay >= 0 & max_delay > min_delay & max_rw > min_rw & x_1 = 0 & x_2 = 0 & x_3 = 0

process_1

start_1 | max_rw >= x_1

lock <> 0 & x_1 > min_rw x_1 := 0;

lock = 0 & x_1 > min_rw x_1 := 0;

set_1 | max_rw >= x_1

lock <> 1 & x_1 > min_delay x_1 := 0;

x_1 > min_rw lock := 1; x_1 := 0;

x_1 := 0; lock := 0;

tryenter_1 | max_delay >= x_1

lock = 1 & x_1 > min_delay

cs_1 | True

process_2

start_2 | max_rw >= x_2

lock <> 0 & x_2 > min_rw x_2 := 0;

lock = 0 & x_2 > min_rw x_2 := 0;

set_2 | max_rw >= x_2

lock <> 2 & x_2 > min_delay x_2 := 0;

x_2 > min_rw lock := 2; x_2 := 0;

x_2 := 0; lock := 0;

tryenter_2 | max_delay >= x_2

lock = 2 & x_2 > min_delay

cs_2 | True

process_3

start_3 | max_rw >= x_3

lock <> 0 & x_3 > min_rw x_3 := 0;

lock = 0 & x_3 > min_rw x_3 := 0;

set_3 | max_rw >= x_3

lock <> 3 & x_3 > min_delay x_3 := 0;

x_3 > min_rw lock := 3; x_3 := 0;

x_3 := 0; lock := 0;

tryenter_3 | max_delay >= x_3

lock = 3 & x_3 > min_delay

cs_3 | True