AndOr.imi

4 clocks	12 parameters	Initial
	dA_High_l	dA_Low_l >= 0
	dA_High_u	& dB_High_l >= 0
	dA_Low_l	& dA_High_l >= 0
	dA_Low_u	& dB_Low_l >= 0
ckOr	dB_High_l	& dOr_l >= 0
ckAnd	dB_High_u	& dAnd_l >= 0
cka	dB_Low_l	& ckOr >= 0
ckb	dB_Low_u	& ckAnd = 0
	dOr_l	& dAnd_u >= dAnd_l
	dOr_u	& dOr_u >= dOr_l
	dAnd_l	& dB_Low_u >= dB_Low_l
	dAnd_u	& dA_Low_u >= dA_Low_l
		& dA_High_u >= dA_High_l
		& cka = 0
		& ckb = 0

input

InputInit | dB_High_u >= ckb

ckb >= dB_High_l
bDown
ckb := 0

Input2 | dA_High_u >= cka

cka >= dA_High_l
aDown
cka := 0

Input3 | dB_Low_u >= ckb

ckb >= dB_Low_l
bUp
ckb := 0

Input4 | dA_Low_u >= cka

cka >= dA_Low_l
aUp
cka := 0
ckb := 0

andGate

And111 | ckAnd >= 0

bDown
ckAnd := 0

bUp

And101 | dAnd_u >= ckAnd & ckAnd >= 0

tDown
ckAnd := 0

tUp
ckAnd := 0

tDown
ckAnd := 0

tUp

And001 | dAnd_u >= ckAnd & ckAnd >= 0

bDown
ckAnd := 0

bUp
ckAnd := 0

And011 | dAnd_u >= ckAnd & ckAnd >= 0

ckAnd >= dAnd_l
xUp

ckAnd >= dAnd_l
xDown

ckAnd >= dAnd_l
xDown

And000 | ckAnd >= 0

tDown

tUp

And100 | ckAnd >= 0

bDown

bUp
ckAnd := 0

And110 | dAnd_u >= ckAnd & ckAnd >= 0

tDown

tUp
ckAnd := 0

bUp bDown

ckAnd >= dAnd_l
xDown

And010 | ckAnd >= 0

orGate

Or111 | ckOr >= 0

aUp

aDown

Or101 | ckOr >= 0

xUp

xDown
ckOr := 0

ckOr >= dOr_l
tDown

ckOr >= dOr_l
tUp

ckOr >= dOr_l
tUp

Or001 | dOr_u >= ckOr & ckOr >= 0

ckOr >= dOr_l
tDown

Or000 | ckOr >= 0

xUp
ckOr := 0

xDown

aUp
ckOr := 0

aDown

Or100 | dOr_u >= ckOr & ckOr >= 0

aUp
ckOr := 0

aDown
ckOr := 0

Or110 | dOr_u >= ckOr & ckOr >= 0

xDown
ckOr := 0

xUp
ckOr := 0

aUp

aDown
ckOr := 0

aUp
ckOr := 0

aDown

Or010 | dOr_u >= ckOr & ckOr >= 0

ckOr >= dOr_l
tUp

Or011 | ckOr >= 0

xDown

xUp

ckOr >= dOr_l
tUp